



1752
EJW

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Bradley J. Howard

Filed: November 6, 2003

For: PROCESS FOR USING PHOTO-DEFINABLE LAYERS IN THE
MANUFACTURE OF SEMICONDUCTOR DEVICES AND
RESULTING STRUCTURES OF SAME

Serial No.: 10/702,238

Group Art Unit: 1752

Examiner: Lee, Sin J

Atty Dkt: 97-0008.02

Pursuant to 37 C.F.R. 1.8, I certify that this correspondence is being deposited with the U.S. Postal Service in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date below:

9-24-04 Diane C Petts
Date Name

RESPONSE TO RESTRICTION REQUIREMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

This paper is submitted as a Response to the Restriction Requirement dated August 25, 2004, a response for which is due on or before September 25, 2004.

Should any fees under 37 CFR 1.16-1.21 be required for any reason relating to the enclosed materials, including any additional fee for an extension of time, the Commissioner is authorized to deduct such fees from O'Keefe, Egan & Peterman Deposit Account No. 10-1205/MTIP:003D2.

Please amend the application as follows.